



Developers Conference 2007

Amsterdam, The Netherlands



Certified Wireless USB Data Transfer and Protocol

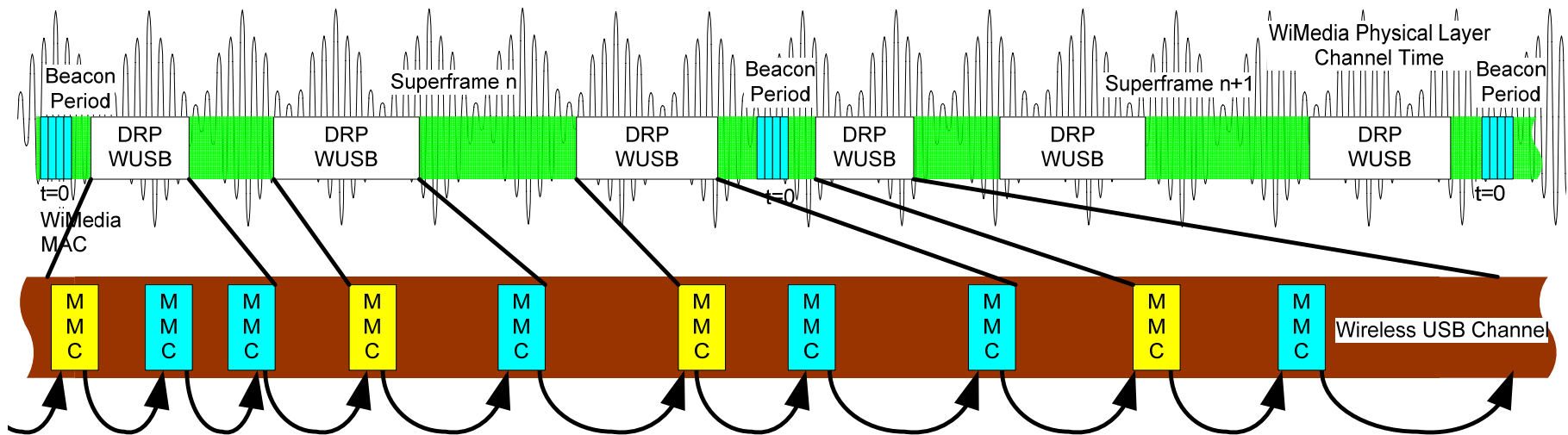
Dan Froelich
Intel Corporation

Agenda



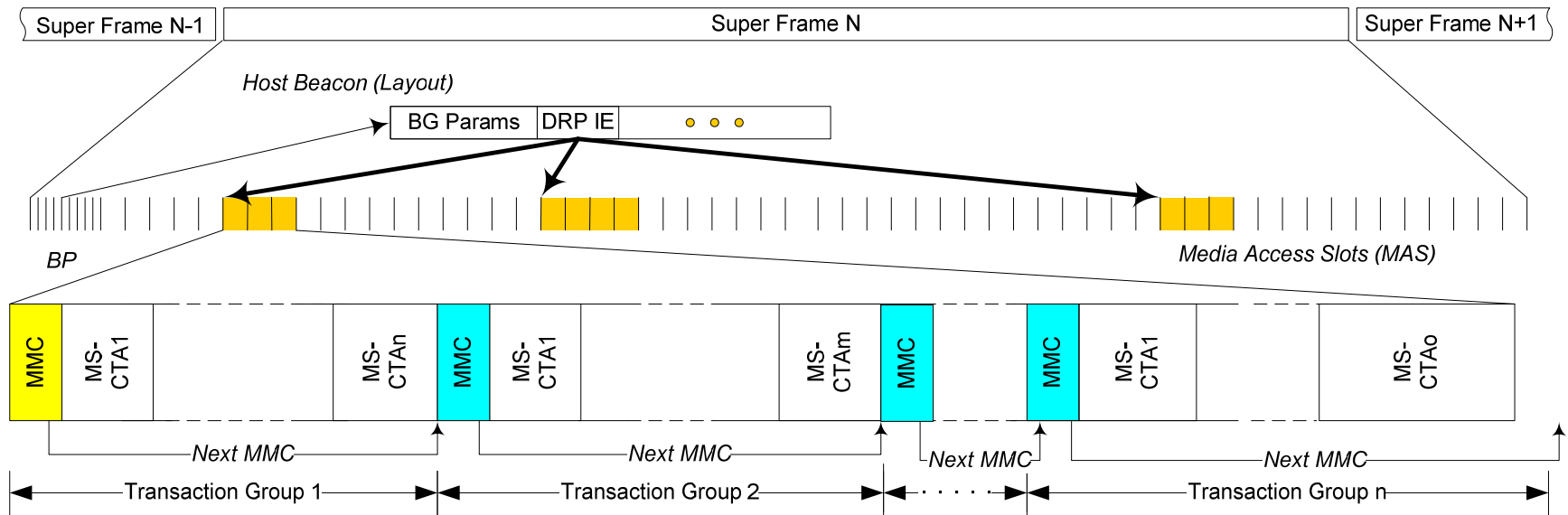
- Data Transfer Model Overview
 - Certified Wireless USB Channel
 - Device Types
 - Transfer Types/Constraints
- Protocol Components
- Data Transfers (general model)
- Device Notifications

Certified Wireless USB Channel



- The Certified Wireless USB Channel is encapsulated by the WiMedia 'channel'
- Uses WiMedia PHY/MAC compliant components
 - WiMedia PHY signaling and frames
 - WiMedia MAC Headers, Security Encapsulation, etc.
- Certified Wireless USB channel is continuous sequence of linked control packets transmitted by the Host during reserved WiMedia channel time
 - CWUSB time is reserved from WiMedia channel time (DRPs)
 - Called MMCs – Micro-schedule Management Commands
- All Certified Wireless USB Data communications are over the CWUSB Channel

Certified Wireless USB Channel Control



- Continuous linked series of MMCs transmitted by Host
- Contain:
 - Certified Wireless USB Channel Time Stamp
 - Time to next MMC
 - General Channel Management Info (Host ID, etc.)
 - Certified Wireless USB channel time allocation declarations
- Transaction Group can not span multiple WiMedia reservation blocks

USB Time



- Wired USB hosts maintain USB channel time
 - In wired USB, HW has 11-bit SOF frame counter providing millisecond resolution or 1/8th millisecond resolution
 - Exported to client drivers through USBDI as 32-bit value
 - In wired USB, time values are primarily used for isochronous traffic
 - SOFs (Start Of Frame packets) pass time information to devices
- Certified Wireless USB hosts also maintain USB channel time
 - 17-bit 1/8th millisecond counter and 7 bit microsecond counter
 - Still exported to client drivers as 32-bit millisecond value
 - Timer used for isochronous and directed beaconing
 - MMCs used to pass time information

Certified Wireless USB Time vs. WiMedia Time



- Certified Wireless USB time is NOT slaved to WiMedia time
- WiMedia superframes will be some number of WUSB microseconds
 - If there is a slow beaconing WiMedia device, superframe may be a few more CWUSB usecs than 64K
- Devices use MMC timestamps to synchronize their internal usec clocks
 - Done on devices that need clocks (isochronous or DBD)
 - DBD must sync transmit packet repetition to WUSB channel time.
 - Device clocks must adjust to host rate

USB Time Across Hierarchy

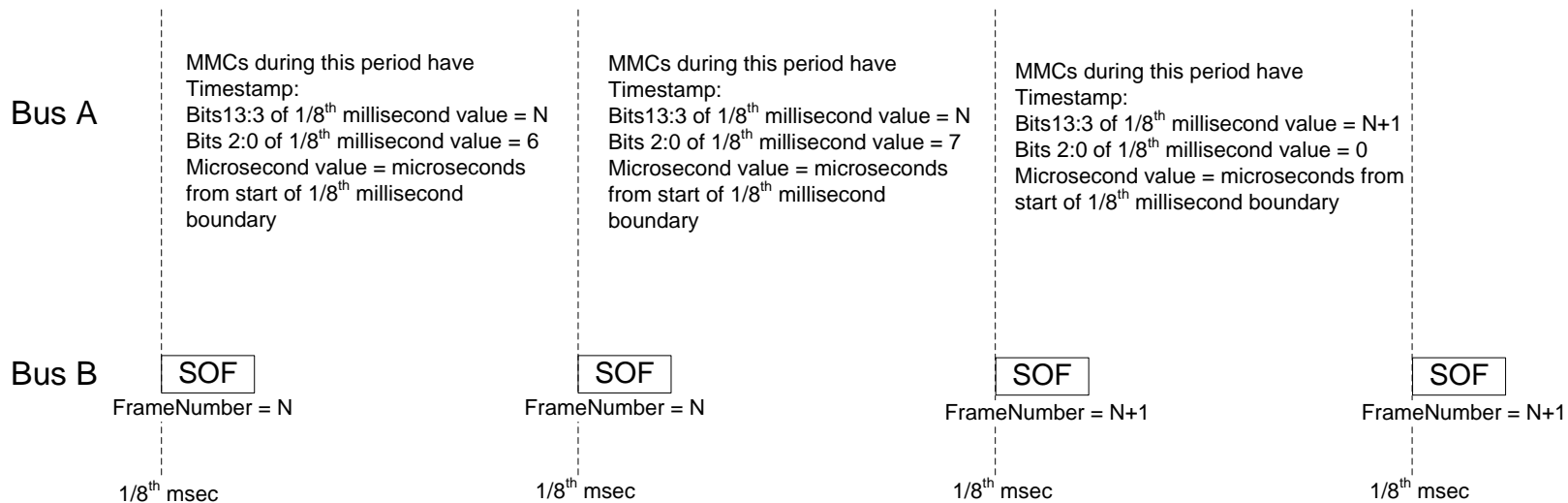


Example Topology



Actual times in MMC and SOFs are the same

Bus Timings



- Device Wire Adapters synch to upstream USB time and propagate to downstream USB time

Certified Wireless USB Cluster



- Term used to refer to devices connected to and communicating via the same Certified Wireless USB Channel
- One Certified Wireless USB Host
- One (or more) Certified Wireless USB Devices
 - Maximum of 128 devices.
 - 128 Addresses used to manage unauthenticated/unassociated devices

Certified Wireless USB Device Types



- Self Beaconsing Devices
 - Devices are full WiMedia MAC aware
 - Attend “Design Considerations for SBDs/DRDs and Multi-Pal Devices” for details
- Non Beaconsing Devices
 - Limited transmit and receive range
 - Fully encompassed by hosts range
 - Not enough detail to be built (yet)



Transfer Type Constraints

- Same Transfer types as USB 2.0
 - Bulk
 - Control
 - Interrupt
 - Isochronous
- Different/new constraints to make more efficient for Wireless Media

Transfer Type Constraints

Bulk



- Retains many/most constraints and behavior of Wired USB
 - Reliable Delivery
 - “Good Effort” service
 - Host may restrict number of data packets (burst size) to provide some level of service in available channel time
 - Short-packet Semantics preserved
- Maximum Packet Size, one of
 - 512, 1024, 1536, 2048, 2560, 3072 and 3584 (only)
- Maximum burst size of 1 to 16
- Use any bit transfer rate supported by device
 - Host choice
- On Flow control event, taken from active list
 - Device must notify host when ‘ready’

Transfer Type Constraints

Control



- Retains many/most constraints and behavior of Wired USB
 - “Best Effort” service
 - Reliable data delivery
- Maximum Packet Size of 512 (only)
- Maximum burst size of 1 data packet per data phase
- Host must use PHY Base rate with Default Control Pipe (Standard Commands)
- On Flow control event, taken from active list
 - Device must notify host when ready

Transfer Type Constraints

Interrupt and Isochronous



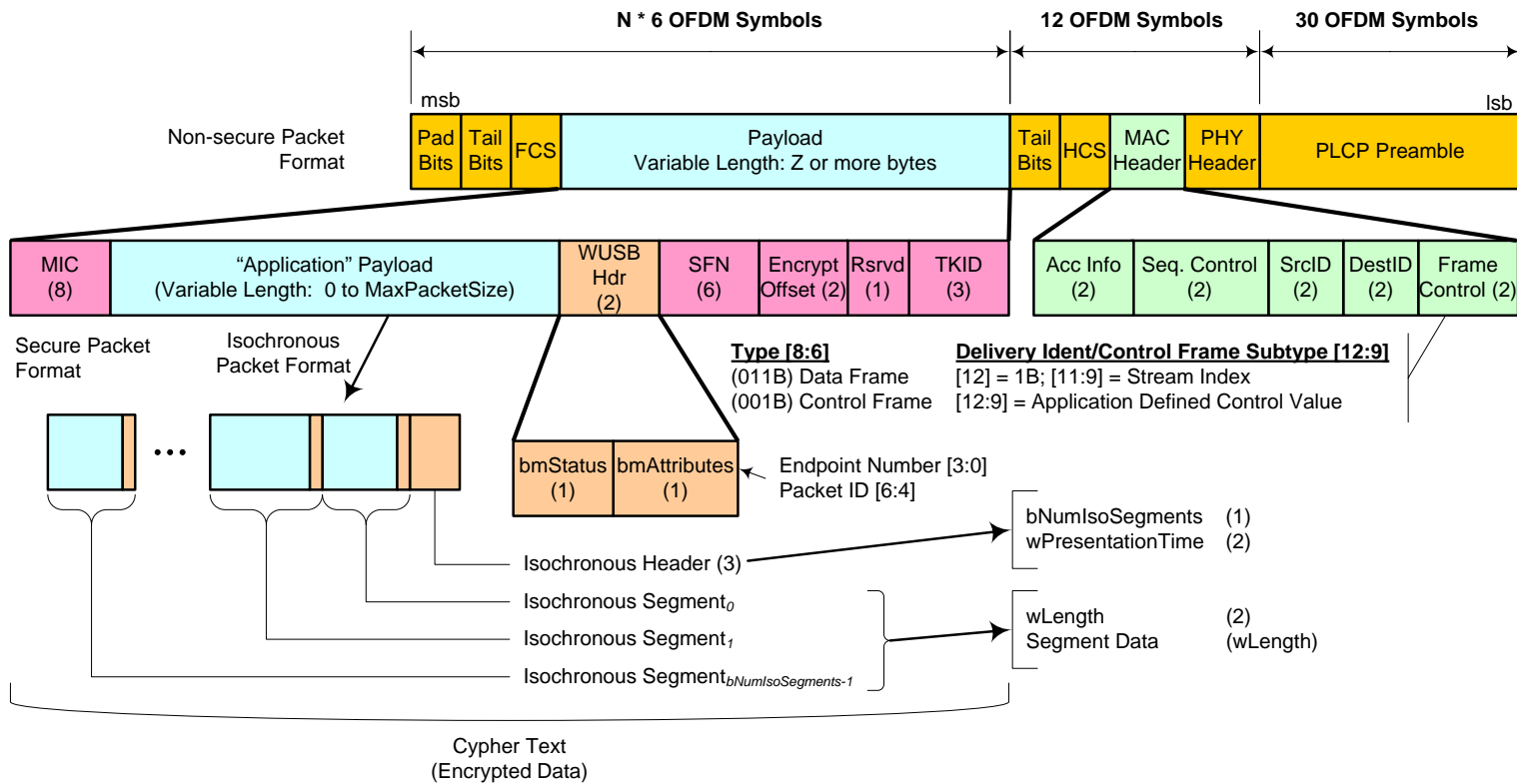
- Not more than 80% of Certified Wireless USB Channel (combined all periodic)
- Similar purpose and characteristics of Wired USB
- On Flow control event host MAY remove from active list
 - Host must resume service next interval.
- See Periodic Transfer models session/presentation for more details.



Agenda

- Data Transfer Model Overview
 - Certified Wireless USB Channel
 - Device Types
 - Transfer Types/Constraints
- Protocol Components
- Data Transfers (general model)
- Device Notifications

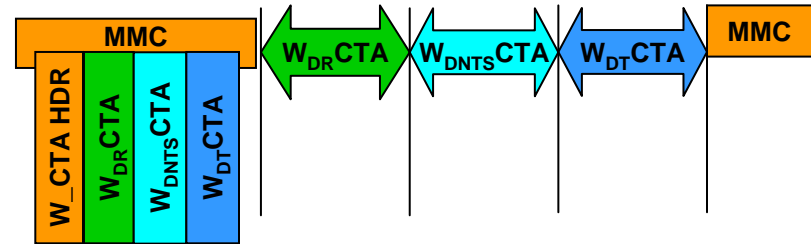
Packet Layout



- WiMedia MAC Packet format
- Utilizes Secure 'frame' format

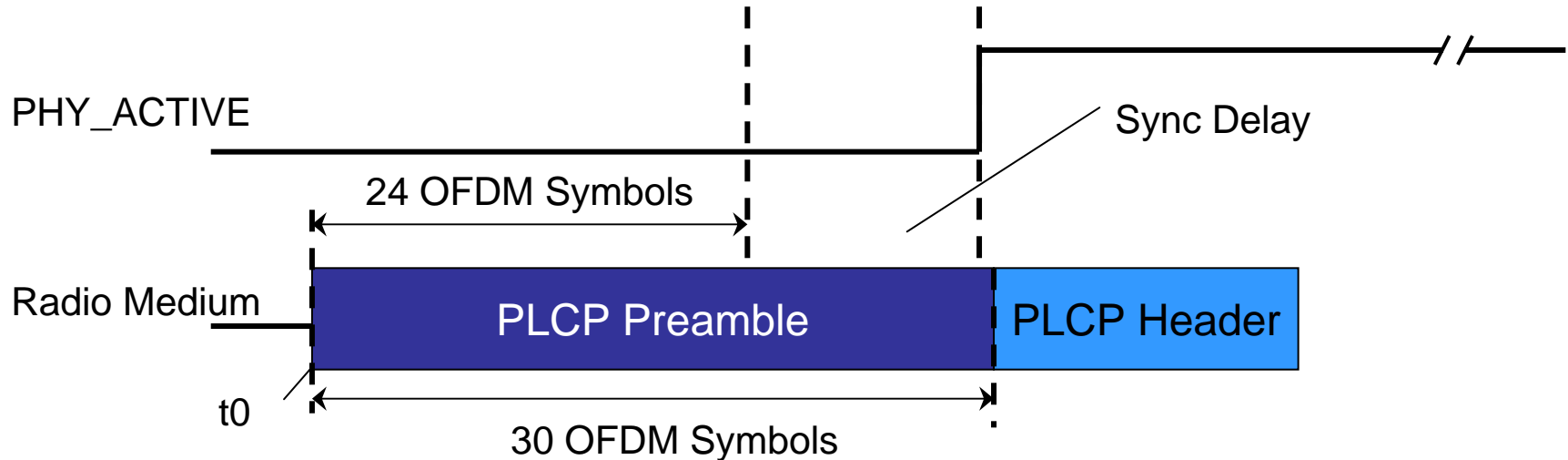
Time Slot Allocations

- Three basic slot types
 - $W_{DR}CTA$ (Device Receive)
 - $W_{DT}CTA$ (Device Transmit)
 - $W_{DNTS}CTA$ (Device Notification Time Slot)
- $W_{DR}CTA$, $W_{DT}CTA$ s are used for Transaction Protocol
 - Control, Bulk, Interrupt and Isochronous data streams
 - Access is : guaranteed time slot
 - Schedule based on client application data flow needs
- $W_{DNTS}CTA$ s used to emulate USB signaling events
 - Connect, disconnect, etc.
 - Access is: contention based - Slotted Aloha
 - Host schedules sufficient number of slots to provide adequate service for the cluster



Transaction Group

Protocol Synchronization (cont.)

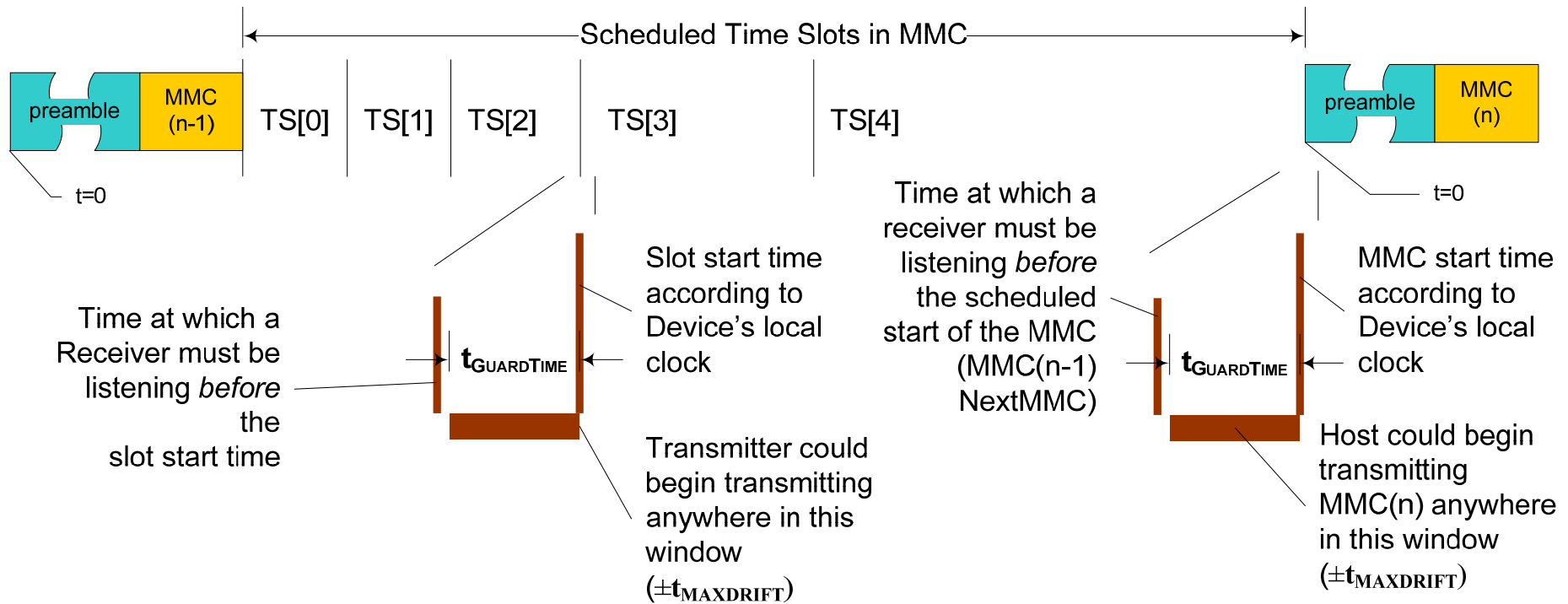


- t_0 is the start time of the first symbol of the MMC's preamble
- Devices can 'calculate' this from the edge of PHY_ACTIVE

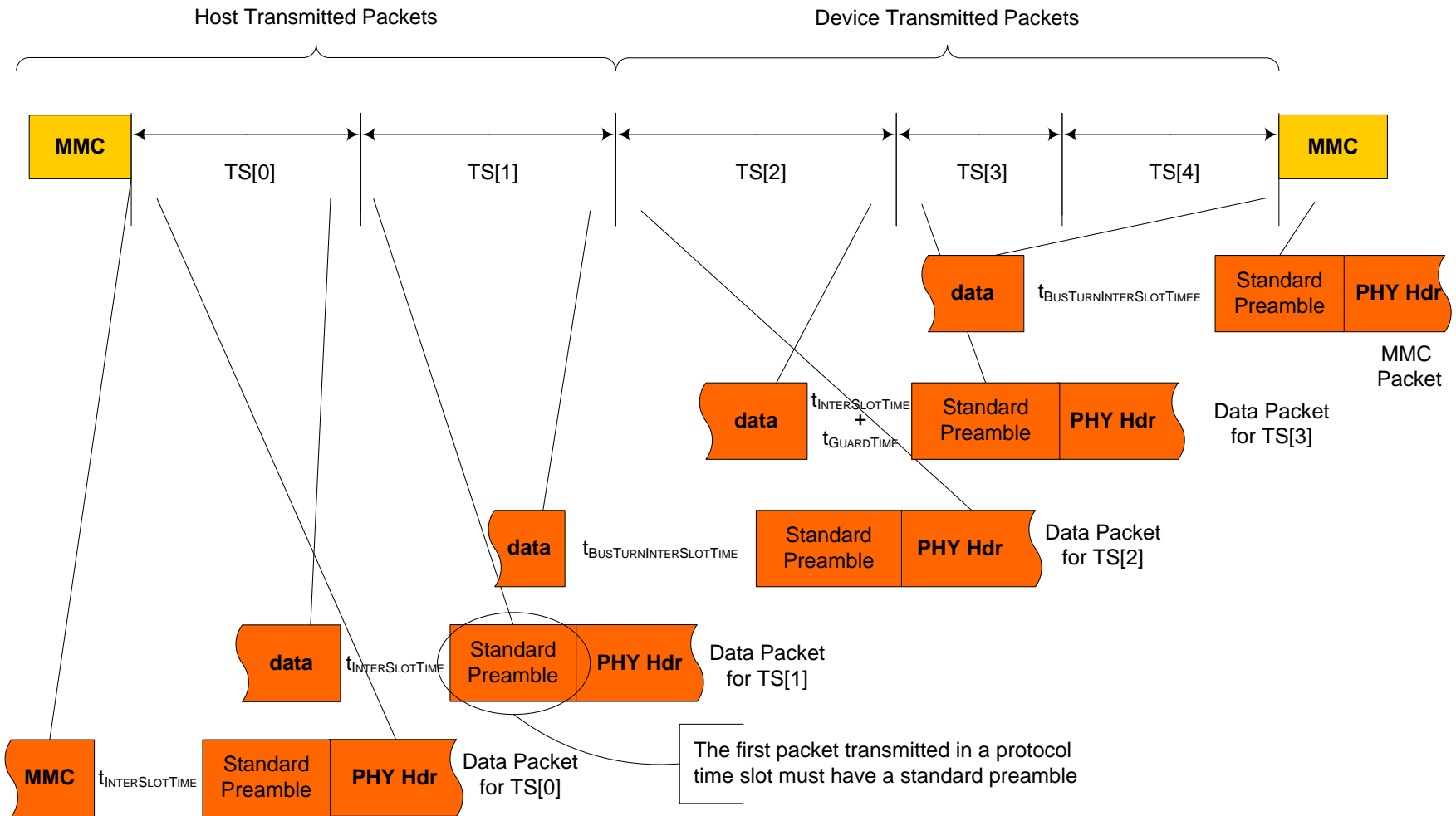
PLCP : Physical layer convergence protocol

OFDM : Orthogonal frequency division multiplexing

Transaction Group Protocol Synchronization



Time Slot Scheduling Requirements



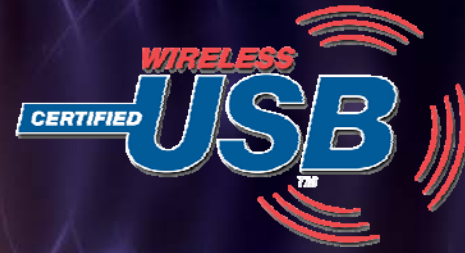


Agenda

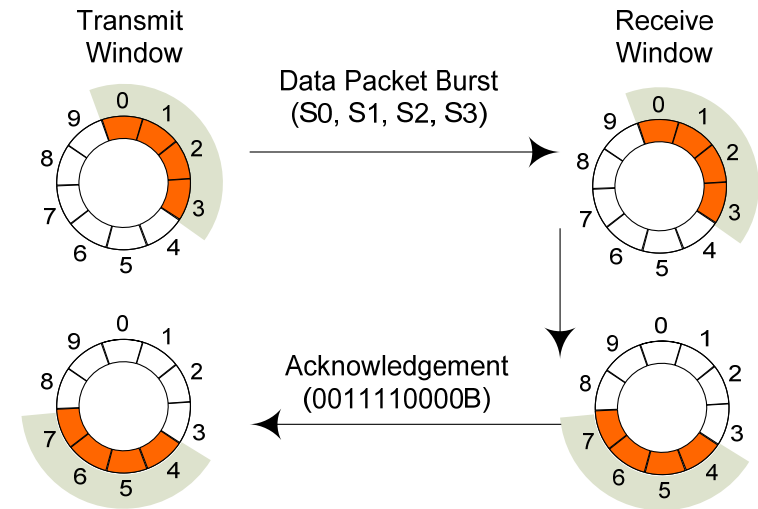
- Data Transfer Model Overview
 - Certified Wireless USB Channel
 - Device Types
 - Transfer Types/Constraints
- Protocol Components
- Data Transfers (general model)
- Device Notifications

Data Stream Synchronization

Basic Model



- Protocol-level support for reliable data delivery
- Identifies:
 - Data ordering requirements
 - Guarantees advancement of data stream only after reliable data delivery
- Terms:
 - Maximum Burst Size
 - Maximum Sequence
 - Maximum Sequence Distance



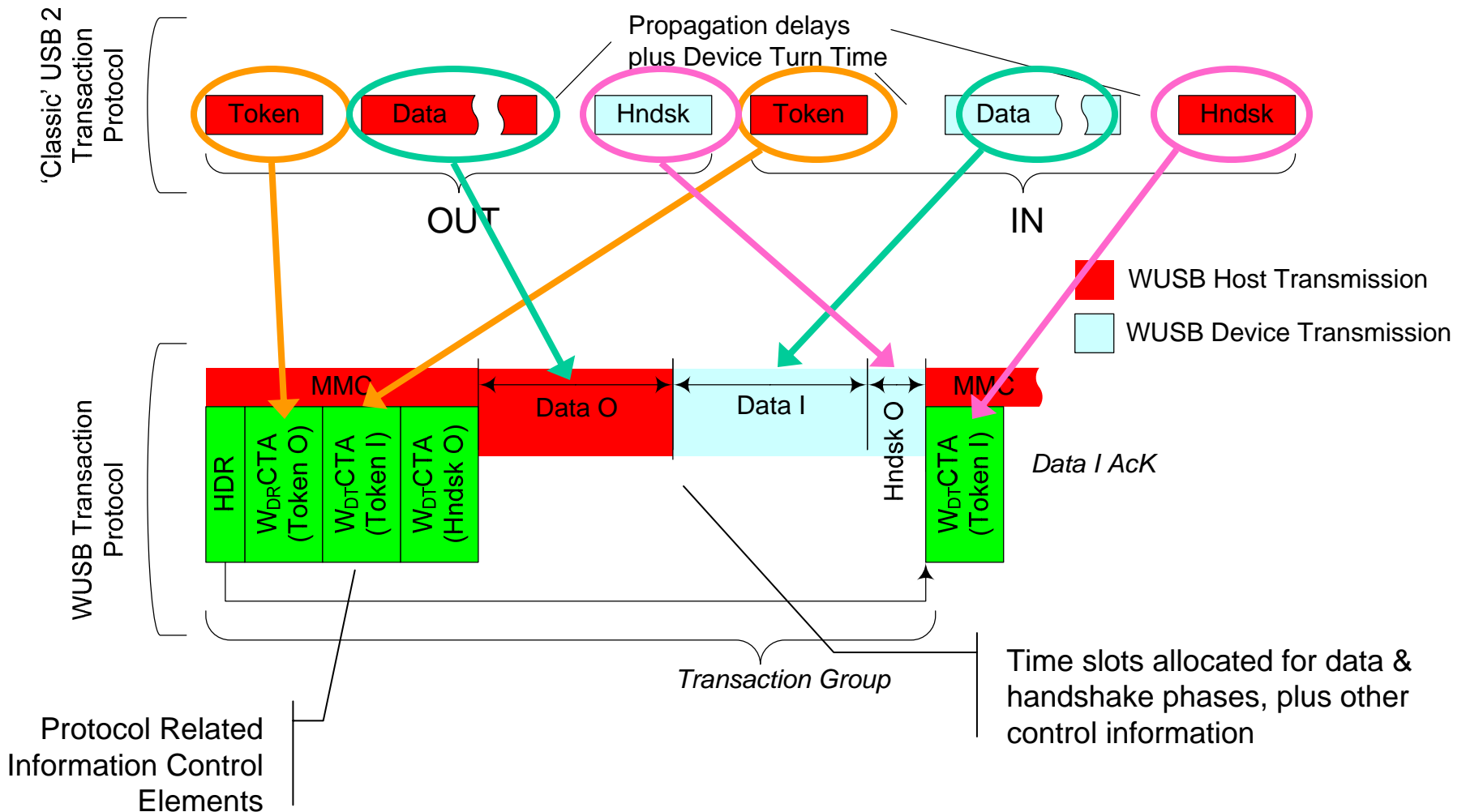
- Transmitter sends data packets associated with Transmit window Sequence numbers
- Receiver acknowledges with new receive window (what is available now)
- Protocol rules for recovering lost packets, avoiding sequence range wrap, etc.

Transaction Transmit Bit Rate



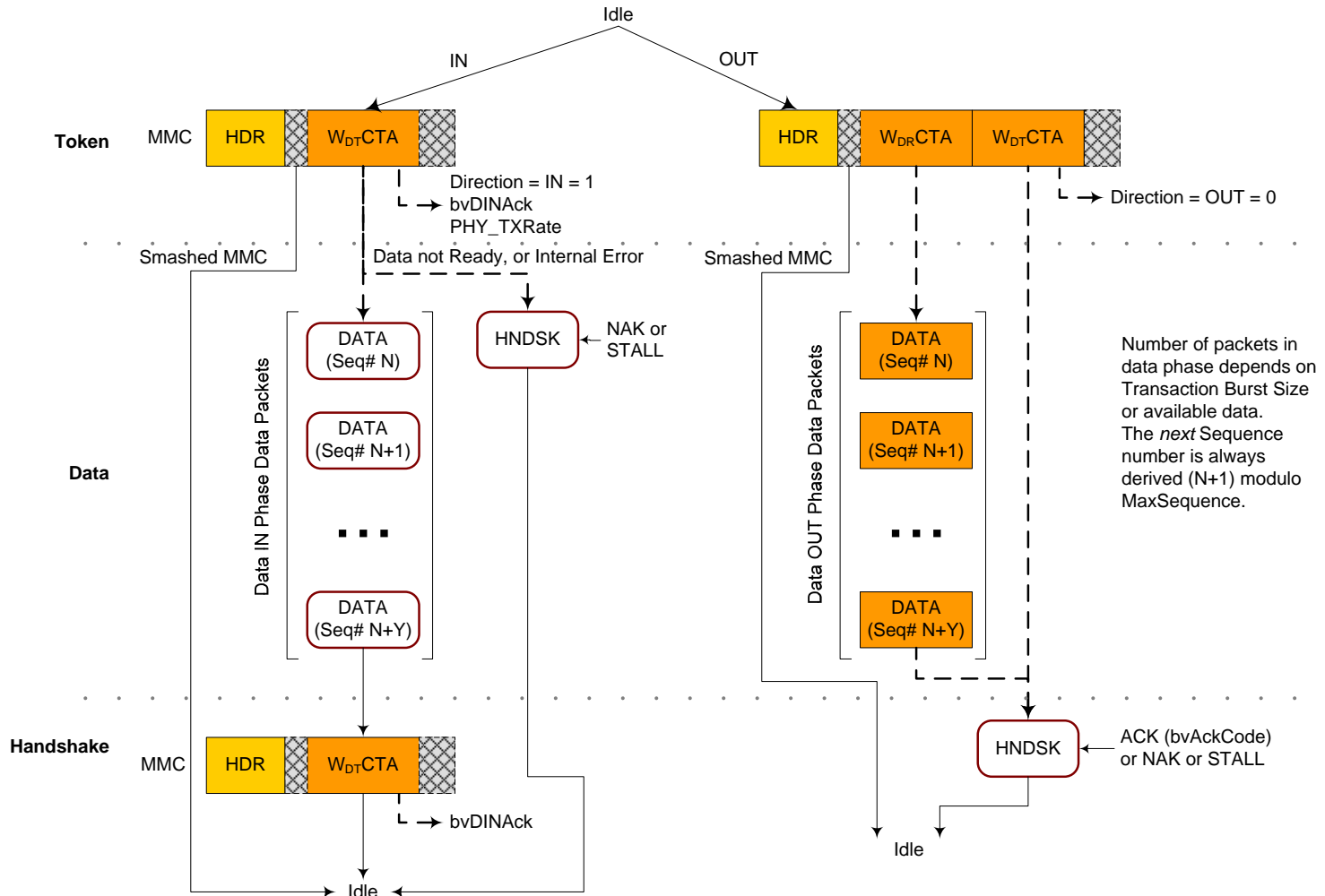
- MMCs are transmitted at ‘base’ signaling rate (53.3 Mb/s)
- Host dictates transmit bit rate for data and handshake phase transmissions
 - Transmit bit rate applies to entire phase
 - OUTs: host uses only TBRs supported by device (assumes Tx/Rx symmetry)
 - INs: $W_{DTCTA.PHY_TXRate}$ directs which TBR device must use during protocol time slot
- Handshakes:
 - Host must use ‘base’ signaling rate for a handshake time slot (OUTs)
 - Host must ensure a data phase time slot is always large enough to accommodate a handshake transmitted at ‘base’ signaling rate

Certified Wireless USB Transactions

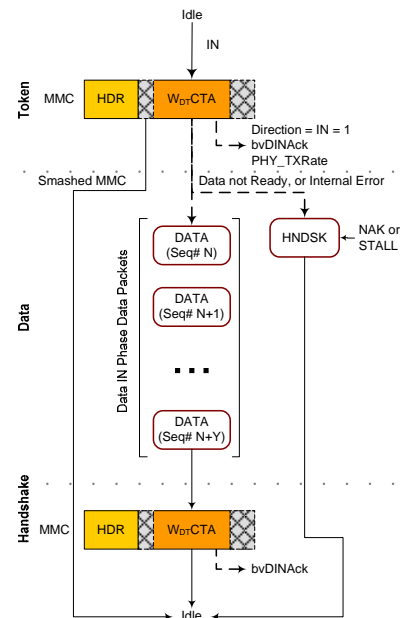


Transaction Format

General Model



Data IN Transfer Streaming Example



USB Packet	Dir	Host Id	Chan TS	IE Id	CTA 0	# Notif TS's	CTA 1	Addr	Endp	EDir	Seq #	Data	Frm Duration	PHY_ACT	Idle
403	MMC	-->	0xBEEF	0:000	WCTA_IE	DNTS	16	DT	1	1	IN	0x00000001			
404	IN	<--	0xBEEF	0	1	1	OUT	0	0	0	0	36 bytes	5.625 µs	7.300 µs	2.386 ms

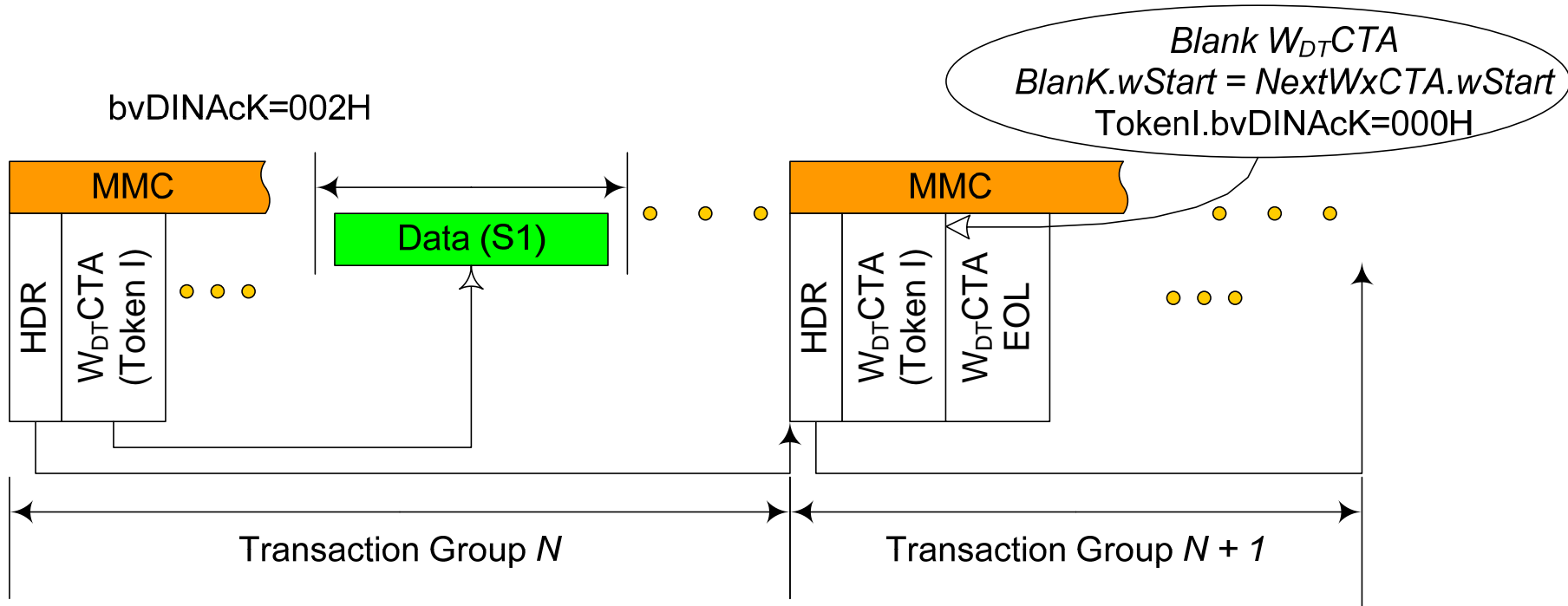
USB Packet	Dir	Host Id	Chan TS	IE Id	CTA 0	# Notif TS's	CTA 1	Addr	Endp	EDir	Seq #	Data	Frm Duration	PHY_ACT	Idle
409	MMC	-->	0xBEEF	0:000	WCTA_IE	DNTS	16	DT	1	1	IN	0x00000002			
410	IN	<--	0xBEEF	0	1	1	OUT	0	0	0	0	13 bytes	5.625 µs	7.300 µs	3.262 ms

WUSB Packet	Dir	Host Id	Chan TS	IE Id	CTA 0	# Notif TS's	CTA 1	Addr	Endp	EDir	Seq #	Data	Frm Duration	PHY_ACT	Idle
416	MMC	-->	0xBEEF	0:000	WCTA_IE	DNTS	16	DT	1	1	IN	0x00000004			
417	IN	<--	0xBEEF	0	1	1	OUT	0	0	0	0	12 bytes	4.375 µs	7.308 µs	2.354 ms

Disclaimer : early prototype host and devices used for traffic capture

Data IN Transfer

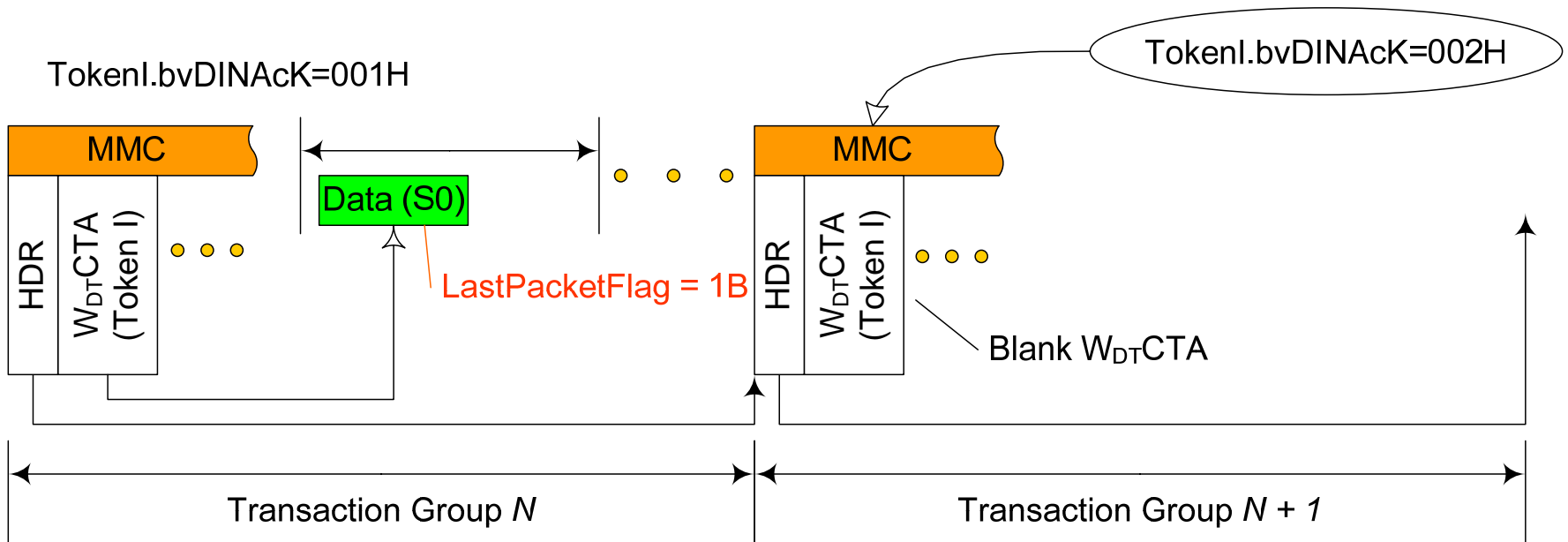
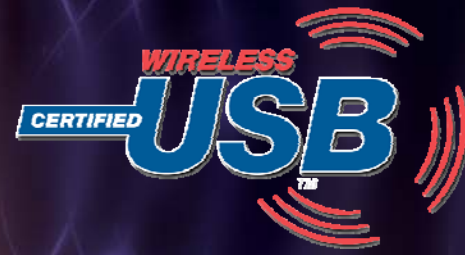
End Of Transfer Example



- At end of Transfer, need to acknowledge last data phase
- Uses 'Blank' IN Token (W_{DTCTA})

Data IN Transfer

Short Packet Example



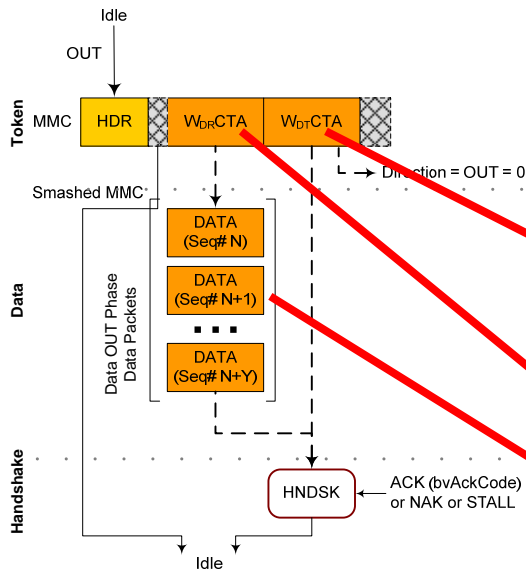
Host may commence next transfer immediately (if buffer available)

Last Packet Flag may only be set on last packet in a burst

Blank W_xCTA contains start time for next CTA.

Data OUT Transfers

Streaming Example



Initial Condition:
Out Endpoint Window (0x0000FFFF)

Data Phase CTA

Handshake Phase CTA

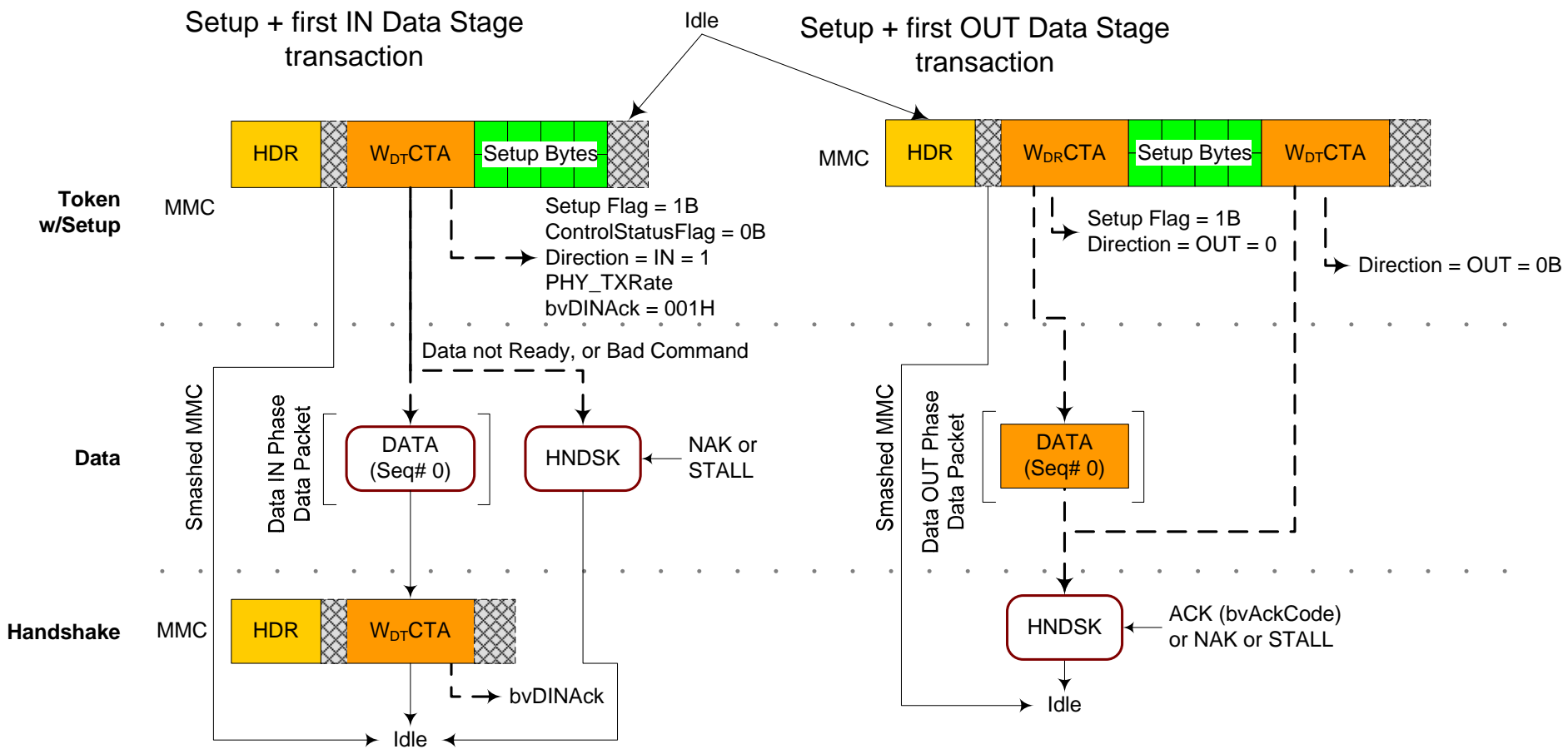
WUSB Packet	MMC	Dir	Host Id	Chan TS	IE Id	CTA 0	Addr	Endp	CTA 1	# Notif TS's	CTA 2	Addr	Endp	EDir	bvAckCode
398	MMC	-->	0xBEEF	0:000	WCTA_IE	DR	1	1	DNTS	16	DT (Hnd)	1	1	OUT	0x00000000
WUSB Packet	OUT	>	0xBEEF	0	DATA	Addr	Endp	EDir	Seq#	Last	Data	Frm Duration	PHY_ACT	Idle	Time Stamp
399	OUT	>	0xBEEF	0	DATA	1	1	OUT	0	1	31 bytes	15.000 µs	10.714 µs	479.668 µs	7.466400520
WUSB Packet	Hand	--	0xBEEF	1	ACK	Addr	Endp	EDir	bvAckCode	Frm Duration	PHY_ACT	Idle	Time Stamp		
400	Hand	--	0xBEEF	1	ACK	1	1	OUT	0x0001FFFE	1.875 µs	7.308 µs	2.630 ms	7.466890902		

Handshake Acks the Seq # (0) by advancing (by one) receive window : $0x0000FFFF << 1 = 0x0001FFFE$

Disclaimer : early prototype host and devices used for traffic capture

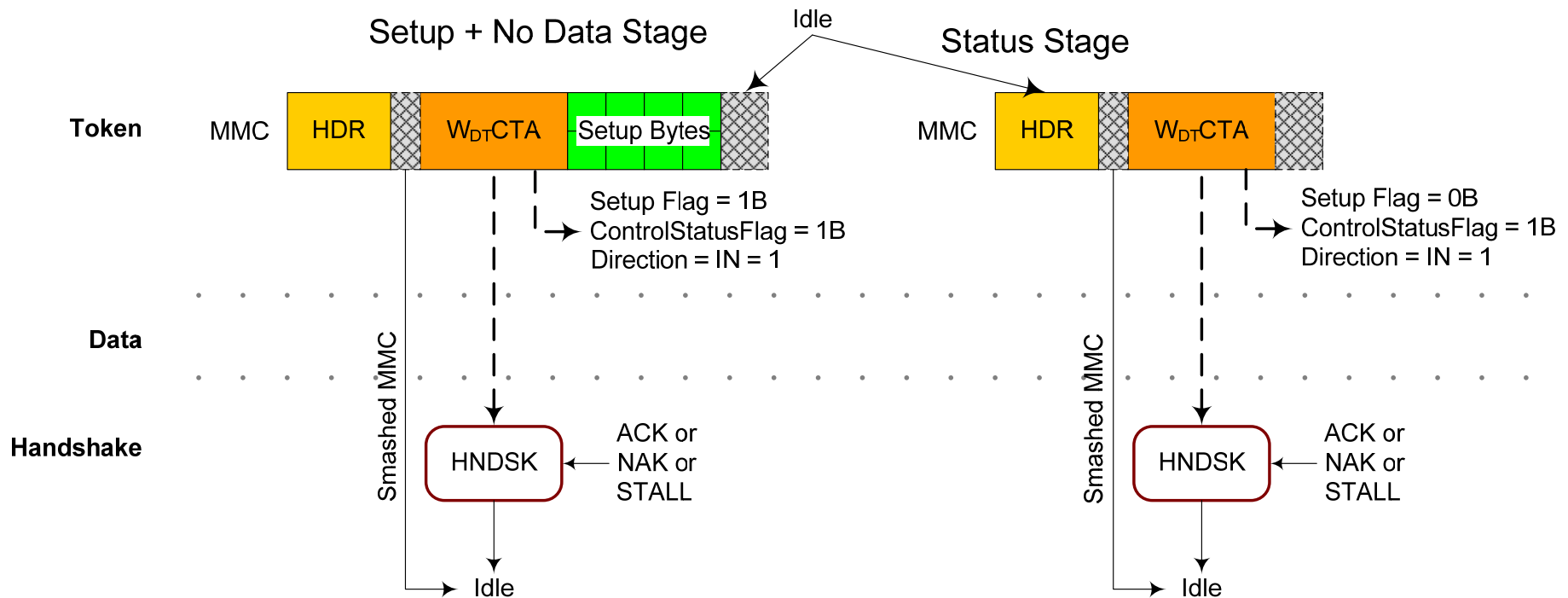
Control Transaction Format

Setup Stage w/Data Stage



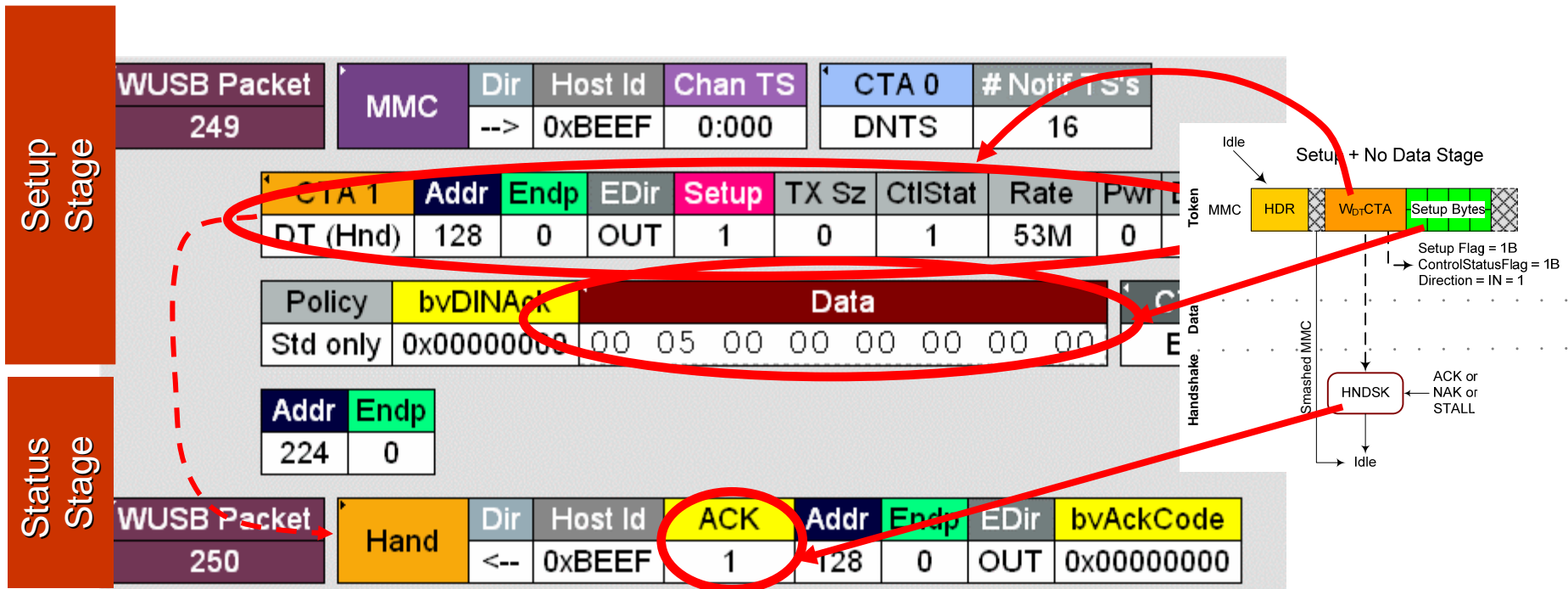
Control Transaction Format

Setup w/No Data Stage; Bare Status Stage



Control Transfer

Example (Set Address - No Data Stage)



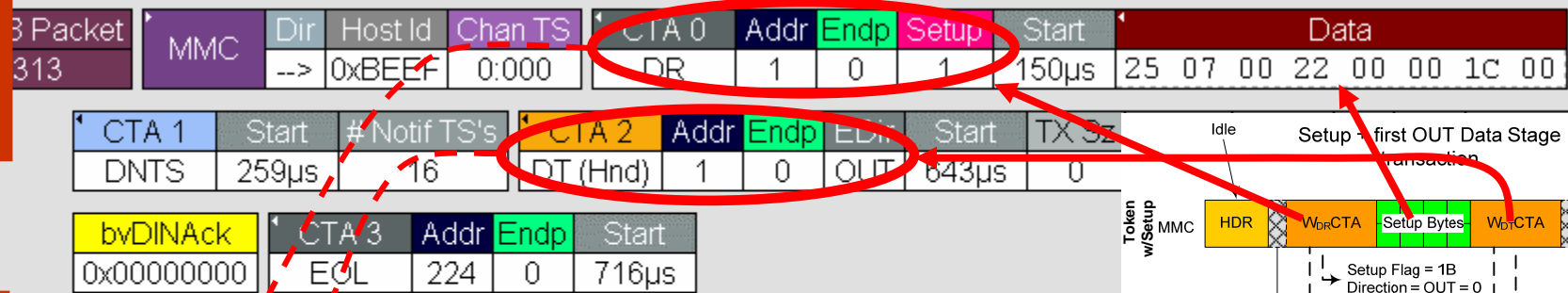
Disclaimer : early prototype host and devices used for traffic capture

Control Transfer

Example (Control Write - SetKey)



Setup Stage

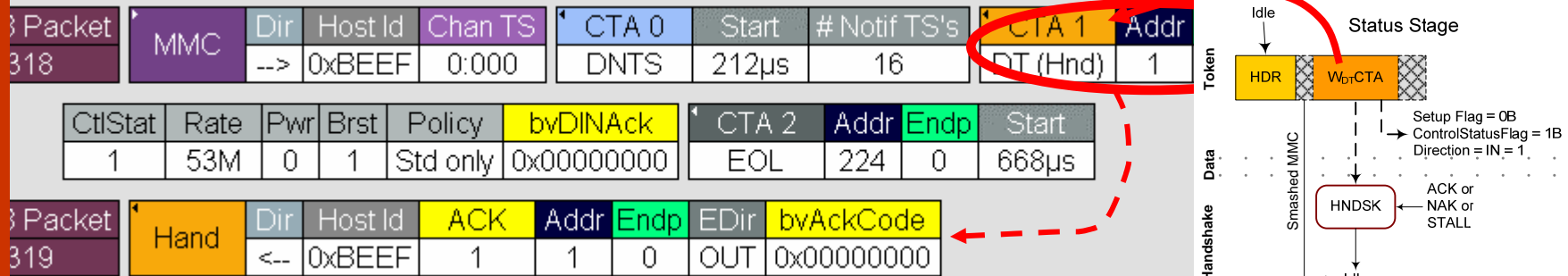


Data Stage



In a subsequent transaction group... the status stage

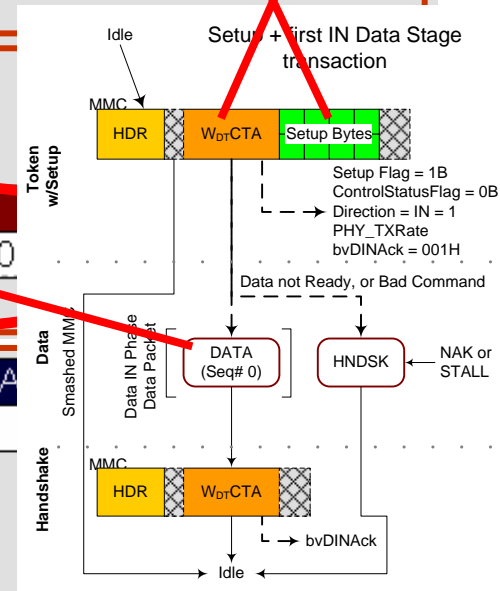
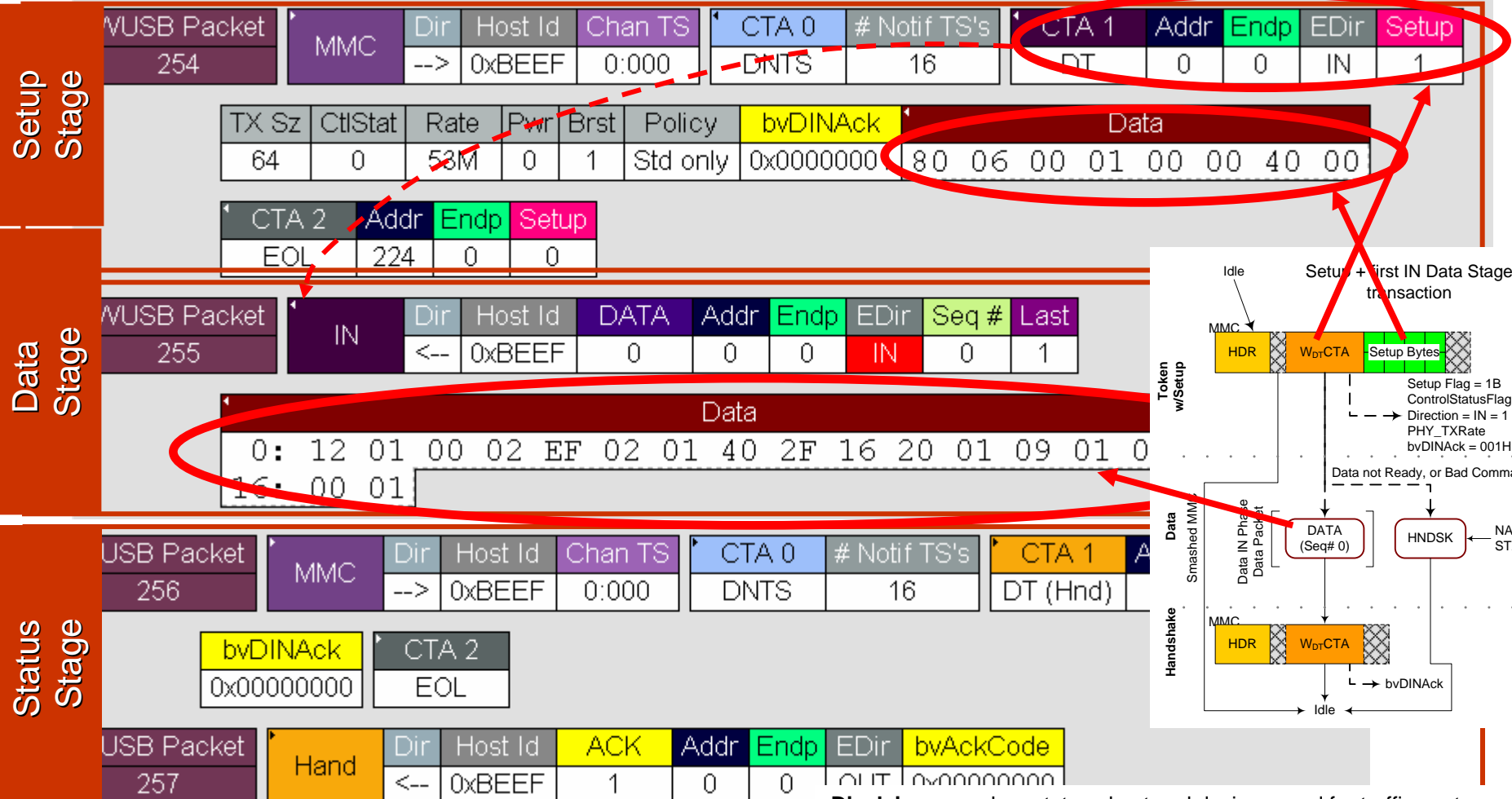
Status Stage



Disclaimer : early prototype host and devices used for traffic capture

Control Transfer

Example (Control Read – Get Descriptor)



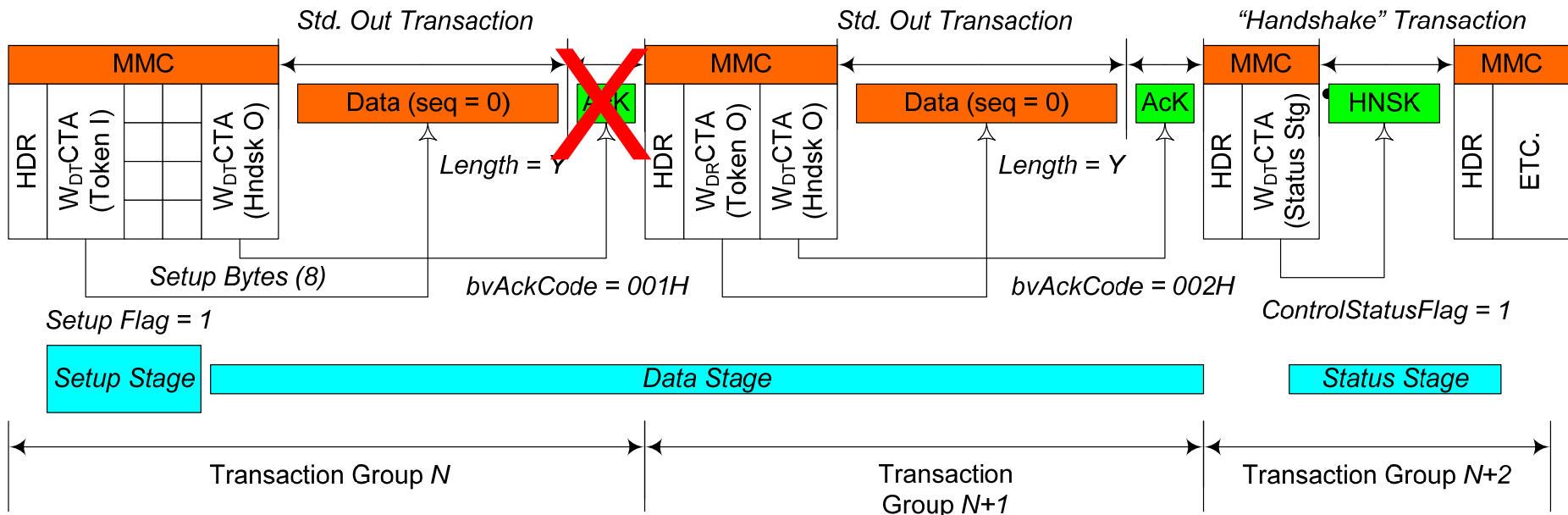
Disclaimer : early prototype host and devices used for traffic capture

Control Transfer

Example (Control Write – Retry Policy)



- No response from device on Setup + OUT
- Retry only first OUT of Data Stage
 - Device response when missed SETUP is a STALL handshake

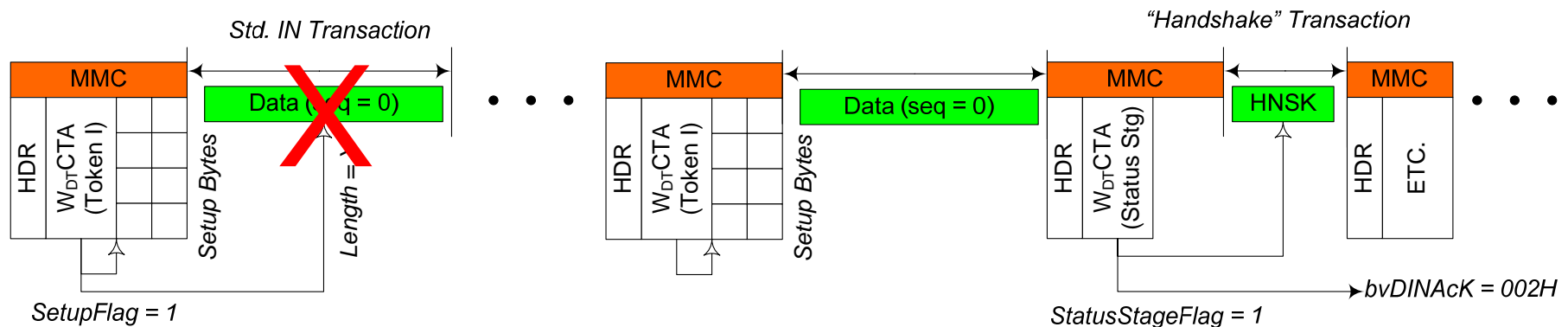


Control Transfer

Example (Control Read – Retry Policy)

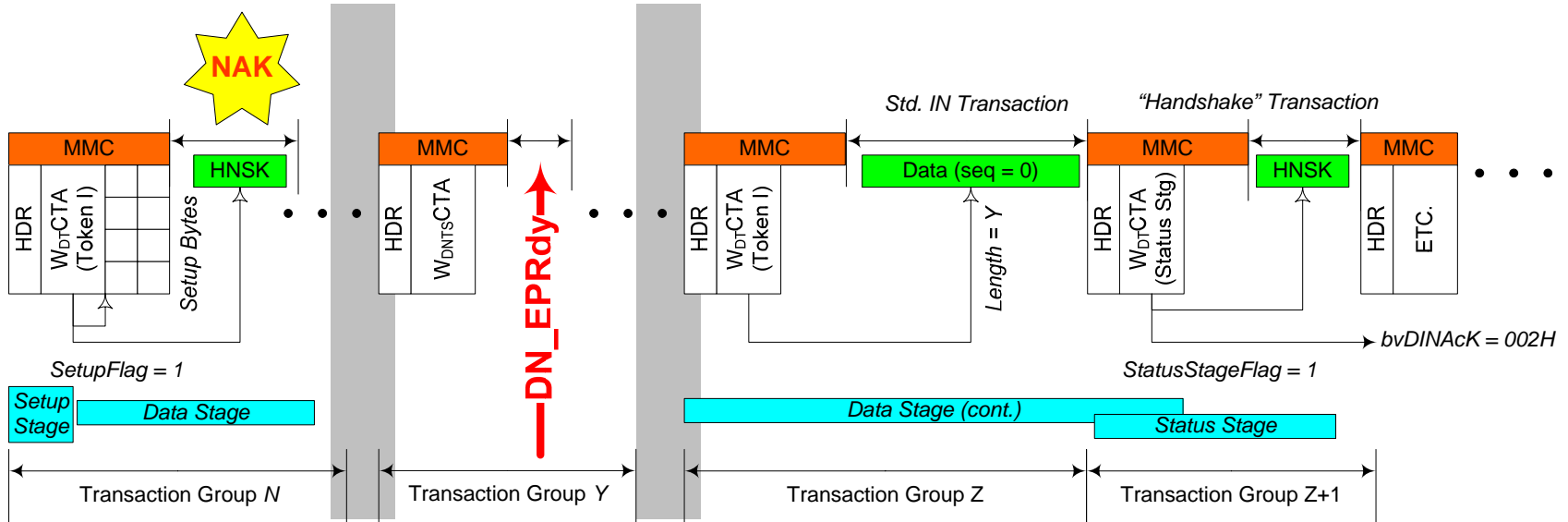


- No response from device on Setup + IN
- Host will retry Setup Stage + IN



Control Transfer

Example (Control Read – Flow Control)



- Host stops polling endpoint on NAK
- Device notifies it is ready with DN_EPRdy
- Host resumes polling endpoint
 - Retries data phase transaction in this example



Agenda

- Data Transfer Model Overview
 - Certified Wireless USB Channel
 - Device Types
 - Transfer Types/Constraints
- Protocol Components
- Data Transfers (general model)
- Device Notifications

Device Notifications Time Slots

DNTS



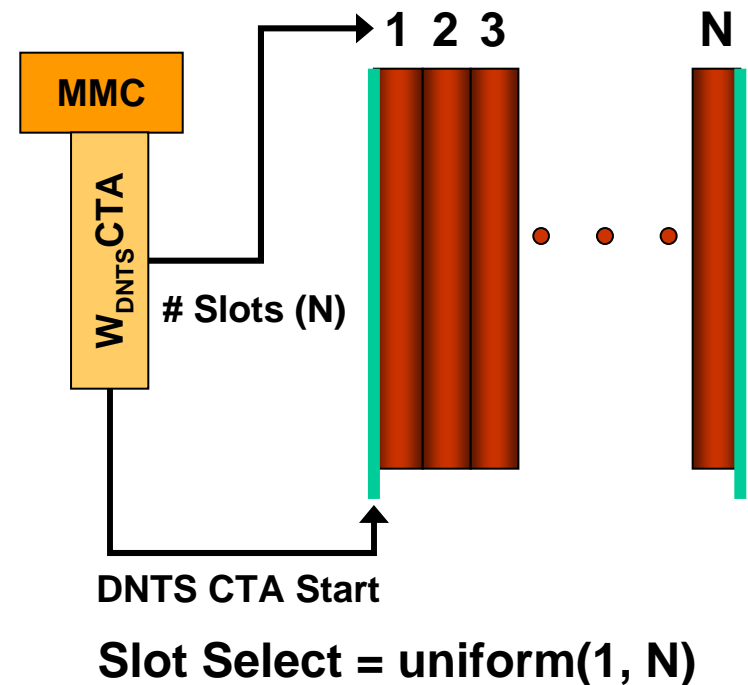
- Devices are only allowed transmitters
- Device notification messages (to the WUSB Host) are only allowed data communications
- Nature of notification messages are that they are asynchronous and infrequent
- Fixed Maximum Size for notification messages
- Device policy for priority of notification messages
- Host policy to manage 'efficiently'
 - Unused time is just lost to CWUSB data stream

DNTS Access Method

Contention Based: Slotted Aloha



- Fixed (maximum) sized message
 - DNTS is a window of uniform sized message slots
- W_{DNTS_CTA} indicates number of message slots
- Device selects a message slot using a uniformly distributed random integer value (in range 1 to N)
- Device transmits message a message slot start time
- Device uses only one slot per W_{DNTS_CTA}

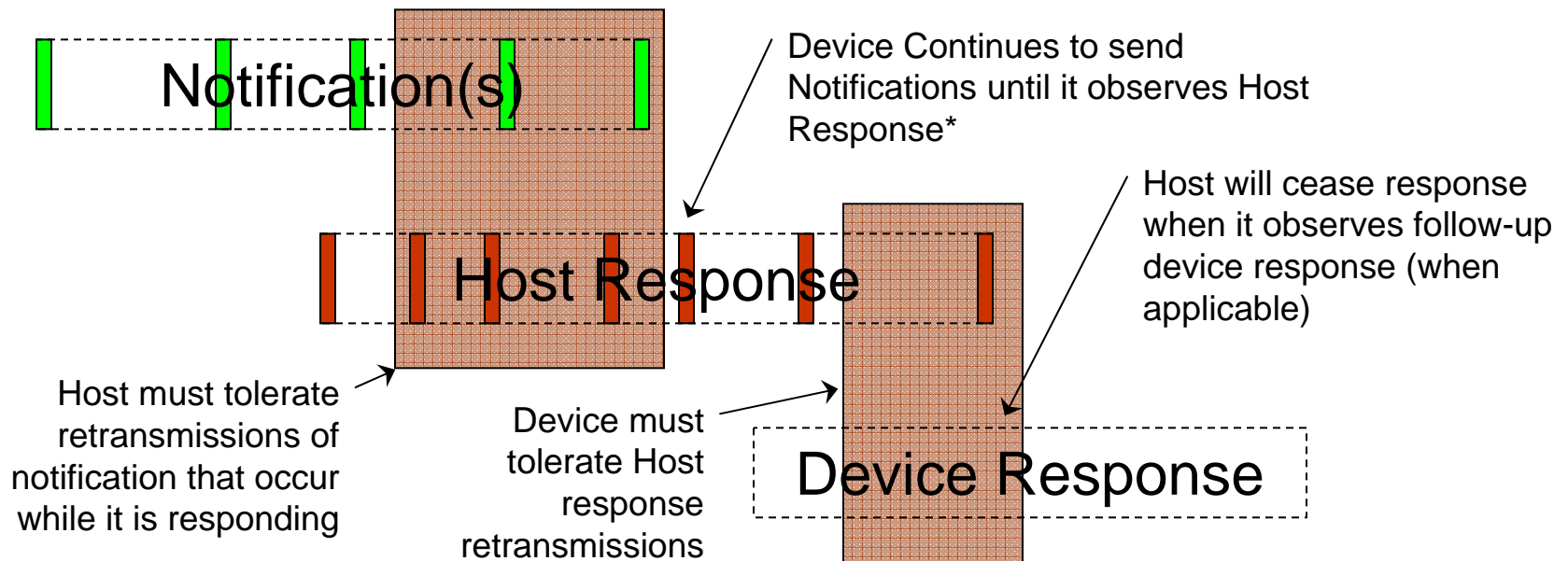


Device Notifications

Reliability



- Protocol for Device Notifications is designed for unreliability of channel



*For Endpoint Ready notifications there is 50 ms on/off cycle



Developers Conference 2007

Amsterdam, The Netherlands