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PLDA Achieves IP Success with Cadence SuperSpeed USB (USB 3.0) Verification IP

High-Quality Results and Predictable Time to Market Cited as Key Benefits

SAN JOSE, Calif., May 18, 2009 – Cadence Design Systems, Inc. (NASDAQ: CDNS), the leader in global electronic design innovation, today announced that PLDA, a leading provider of semiconductor intellectual property (IP) specializing in high-speed interconnect protocols and technologies, utilized Cadence® Incisive® USB 3.0 (SuperSpeed USB) [verification IP](#) (VIP) to achieve IP success for its commercial USB 3.0 design IP. PLDA said the VIP helps ensure top-quality results are delivered within time-to-market requirements.

Cadence USB 3.0 VIP improves verification quality, predictability and productivity. Part of the company’s broad-ranging VIP portfolio encompassing over 30 protocols, the SuperSpeed USB VIP provides the ability to both measure functional coverage and to attain much higher levels than alternative approaches. [Introduced in November](#) 2008, the VIP is compliant with the Open Verification Methodology (OVM). It supports the major IEEE-ratified testbench languages, including SystemVerilog, e, Verilog and VHDL.

“The Cadence SuperSpeed USB VIP is helping PLDA to be one of the first IP vendors to reach the market, while ensuring it will be among the highest quality products available,” said Stephane Haradou, chief technology office of PLDA, a leading provider of semiconductor IP specialized in high-speed interconnect protocols and technologies. “Using the Cadence VIP is enabling PLDA to achieve significantly greater functional coverage results.”

Jeff Ravencraft, president and chairman, USB-Implementers Forum, said “The USB-IF is pleased to have Cadence as a SuperSpeed USB verification IP provider to help speed the delivery of high quality SuperSpeed USB devices to the marketplace.”

Cadence's commitment to USB engineering teams is underscored by its sponsorship of and participation in the [SuperSpeed USB Developers Conference](#) in Tokyo, Japan May 20 and 21.

About Cadence

Cadence enables global electronic design innovation and plays an essential role in the creation of today's integrated circuits and electronics. Customers use Cadence software and hardware, methodologies, and services to design and verify advanced semiconductors, consumer electronics, networking and telecommunications equipment, and computer systems. The company is headquartered in San Jose, Calif., with sales offices, design centers, and research facilities around the world to serve the global electronics industry. More information about the company, its products, and services is available at www.cadence.com.

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