

PRESS RELEASE

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SLS releases SuperSpeed USB Device IP Core and Software

Solutions which allow companies to bring SuperSpeed products to market faster.

LAS VEGAS – January 5, 2011 – System Level Solutions, Inc (SLS), a leader in IP core development, today announced the release of a flexible SuperSpeed USB Device IP Core and software. As the number of SuperSpeed USB (USB 3.0) products continue to dramatically increase, so too is the need to deliver customized interface capabilities for the next generation of SuperSpeed consumer products.

“SuperSpeed USB offers lightning-fast data transfer rates making it ideal to design into a number of varied products,” said Paresh Patel, SLS President and CEO. “Our offering of the IP core for FPGA technology with a path to ASIC technology allows developers to quickly get to market with a silicon solution and peace of mind that a development board and software is also part of the delivered package.”

The package SLS provides includes a development board with an Altera FPGA, ONFI NAND Flash, SDCard, and a high-speed connector. The board, along with the software, helps developers realize their product such as camera, solid state storage, data acquisition or any other custom function using a SuperSpeed interface.

About SLS:

System Level Solutions provides unprecedented creativity and integration know how that helps designers maneuver the pitfalls inherent in combining disparate hardware, software and IP. SLS not only provides IP as a core competency, but also design services and manufacturing. SLS offers a wide-range of capabilities including: USB 3.0, USB 2.0, SD Host and many more cores, development boards, software and services.