

USB4 1.0 ENGINEERING CHANGE NOTICE FORM

Title: TBT DROM Base Address Change
Applied to: USB4 Specification Version 1.0

Brief description of the functional changes:

Limits the TBT3 Device DROM Base address to start at 0000_0001h rather than 0000_0000h.

Benefits as a result of the changes:

Improved interoperability with existing TBT3 Connection Managers that consider a DROM Base Address equal to zero as no DROM.
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An assessment of the impact to the existing revision and systems that currently conform to the USB specification:
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None

An analysis of the hardware implications:
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None

An analysis of the software implications:
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None

An analysis of the compliance testing implications:
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Update the CTS to check the modified range.

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Actual Change

(a). Table 13-14

Table 13-14. Vendor Specific 1 Capability Fields

DW	Register Name	Bit(s)	Field Name and Description	Type	Default Value
12	VSC_CS_12	31:0	DROM Base Address This field shall contain the base address (in bytes) of the DROM within the Flash Memory address space. This field shall be between 0000_0000h <u>0000_0001h</u> and 0000_FFFFh.	RO	Vendor Defined