# USB4 1.0 ENGINEERING CHANGE NOTICE FORM

## Title: Read Response from Register 12 in TBT3 Router
## Applied to: USB4 Specification Version 1.0

| **Brief description of the functional changes:** |
|---|
| Describes the optional response from a TBT3 Router when accessing its Sideband Register 12. |

| **Benefits as a result of the changes:** |
|---|
| A better description of the expected behavior when getting a 2 bytes response from a TBT3 Router. |

| **An assessment of the impact to the existing revision and systems that currently conform to the USB specification:** |
|---|
| None |

| **An analysis of the hardware implications:** |
|---|
| None |

| **An analysis of the software implications:** |
|---|
| None |

| **An analysis of the compliance testing implications:** |
|---|
| None |

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## Actual Change

## (a). Section 13.2.1.3 SB Register Space, Page 541

### To Text:

A TBT3 Router may respond to a read from Register 12 with either 2 or 3 bytes. In the case of a 2-byte response, a Router shall infer that *USB4 Sideband Channel Support* is 0b and *TBT3-Compatible Speeds Support* is 1b.