

USB4 1.0 ENGINEERING CHANGE NOTICE FORM

Title: DROM Base Address

Applied to: USB4 Specification Version 1.0

Brief description of the functional changes:

Limits the DROM Base address to use only 16 bits, as the TBT3 CM reads only the first Word out of this Doubleword.

Benefits as a result of the changes:

New devices that implement TBT3 Compatibility will interoperate correctly with TBT3 CM.

An assessment of the impact to the existing revision and systems that currently conform to the USB specification:

None

An analysis of the hardware implications:

None

An analysis of the software implications:

None

An analysis of the compliance testing implications:

None

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Actual Change

(a). Table 13-14. Vendor Specific 1 Capability Fields

Table Error! No text of specified style in document.-1. Vendor Specific 1 Capability Fields

DW	Register Name	Bit(s)	Field Name and Description	Type	Default Value
12	VSEC_CS_12	31:0	DROM Base Address This field shall contain the base address (in bytes) of the DROM within the Flash Memory address space- <i>This field shall be between 0000_0000h and 0000_FFFFh.</i>	RO	Vendor Defined