USB4 1.0 ENGINEERING CHANGE NOTICE FORM

Title: Change in ISC and ISS Registers Addresses
Applied to: USB4 Specification Version 1.0

Brief description of the functional changes:
Changes the addresses of ISC and ISS registers in the Host Interface Memory BAR Registers

Benefits as a result of the changes:
Fixes a mistake in the addresses

An assessment of the impact to the existing revision and systems that currently conform to the USB specification:
None

An analysis of the hardware implications:
Address change

An analysis of the software implications:
Address change

An analysis of the compliance testing implications:
Change addresses

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Actual Change Requested

(a). Table 12-10, Page 496

From Text:

Interrupts		
37800h : 37800h + (4 * ceiling(3N/32) − 1)	Interrupt Status (ISR)	12.6.3.4.1
3780Ch : 3780Ch + (4 * ceiling(3N/32) − 1)	Interrupt Status Clear (ISC)	12.6.3.4.2
37814h : 37814h + (4 * ceiling(3N/32) − 1)	Interrupt Status Set (ISS)	12.6.3.4.3
38200h : 38200h + (4 * ceiling(3N/32) − 1)	Interrupt Mask (IMR)	12.6.3.4.4

To Text:

Interrupts		
37800h : 37800h + (4 * ceiling(3N/32) − 1)	Interrupt Status (ISR)	12.6.3.4.1
37808Ch : 37808Ch + (4 * ceiling(3N/32) − 1)	Interrupt Status Clear (ISC)	12.6.3.4.2
378104h : 378104h + (4 * ceiling(3N/32) − 1)	Interrupt Status Set (ISS)	12.6.3.4.3
38200h : 38200h + (4 * ceiling(3N/32) − 1)	Interrupt Mask (IMR)	12.6.3.4.4