

# USB4 1.0 ENGINEERING CHANGE NOTICE FORM

**Title: CL0s Exit Latency Change**  
**Applied to: USB4 Specification Version 1.0**

<b>Brief description of the functional changes:</b>
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Updates the CL0s exit latency to account for a change to the Re-timer timing requirement in the USB4 Re-Timer Specification.
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<b>Benefits as a result of the changes:</b>
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Matches the Re-timer specification.
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<b>An assessment of the impact to the existing revision and systems that currently conform to the USB specification:</b>
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None
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<b>An analysis of the hardware implications:</b>
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None
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<b>An analysis of the software implications:</b>
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None
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<b>An analysis of the compliance testing implications:</b>
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None
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## Actual Change

### (a). 4.2.1.6.5.1 Exit flow from CL0s state, Page 160

#### From Text:

In order to limit the CL0s exit time to 230 $\mu$ s, a Router shall comply with the following equation:

#### To Text:

In order to limit the CL0s exit time to ~~230~~245 $\mu$ s, a Router shall comply with the following equation: